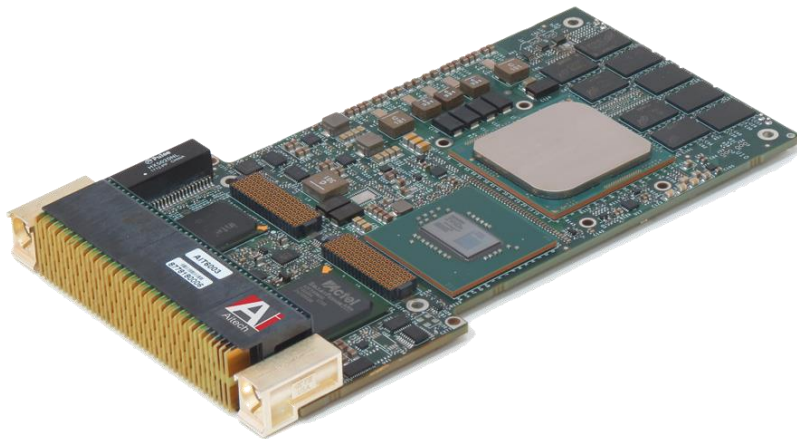


U-C8770 | Secure HPEC SBC

3U VPX w/Intel® Xeon® D & Xilinx® MPSoC FPGA



*U-C8770 is based on our award-winning C877 SBC, and is aligned with the **Open Group SOSA™** technical standard*

- Rugged 3U VPX Single-Slot SBC
- Aligned with the SOSA™ standard
 - ▶ Supports I/O Intensive SBC slot profile
 - ▶ Both PCIe x4 and 40GE data plane options
 - ▶ PCIe x4 expansion plane
- Intel Xeon-D 1500 series CPU with up to 16 cores/32 threads
- Up to 32 GB DDR4 w/ECC @ 2133 MT/s
- Up to 1 TB On-Board SATA SSD, with Encryption, Quick Erase, and Secure Erase
- AiSecure™ Cyber Security Framework
- Xilinx UltraScale+ FPGA w/ARM CPU
 - ▶ Custom I/O and Security Features
 - ▶ PCIe x8 Gen3 Link to Xeon CPU
 - ▶ 40GE data plane via optional FPGA IP core
- XMC Slot w/PCIe x8 Gen3
 - ▶ P1w9-X12d+X16s+X8d XMC I/O mapping
- Versatile Board I/O
 - ▶ USB 3.0 & 2.0 ▶ Serial ▶ 1GE
 - ▶ SATA III ▶ GPIO ▶ 10GE
- WWDT, ETR, RTC, Temp. Sensors
- IPMI, VITA 46.11 Tier 1 & Tier 2 Support
- Windows®, Linux®, VxWorks® Support
- 2LM Option per VITA 48.2
- Conduction and Air-Cooled Versions
- Vibration and Shock Resistant

SOSA™
Sensor Open Systems Architecture

MEMBER

PRELIMINARY



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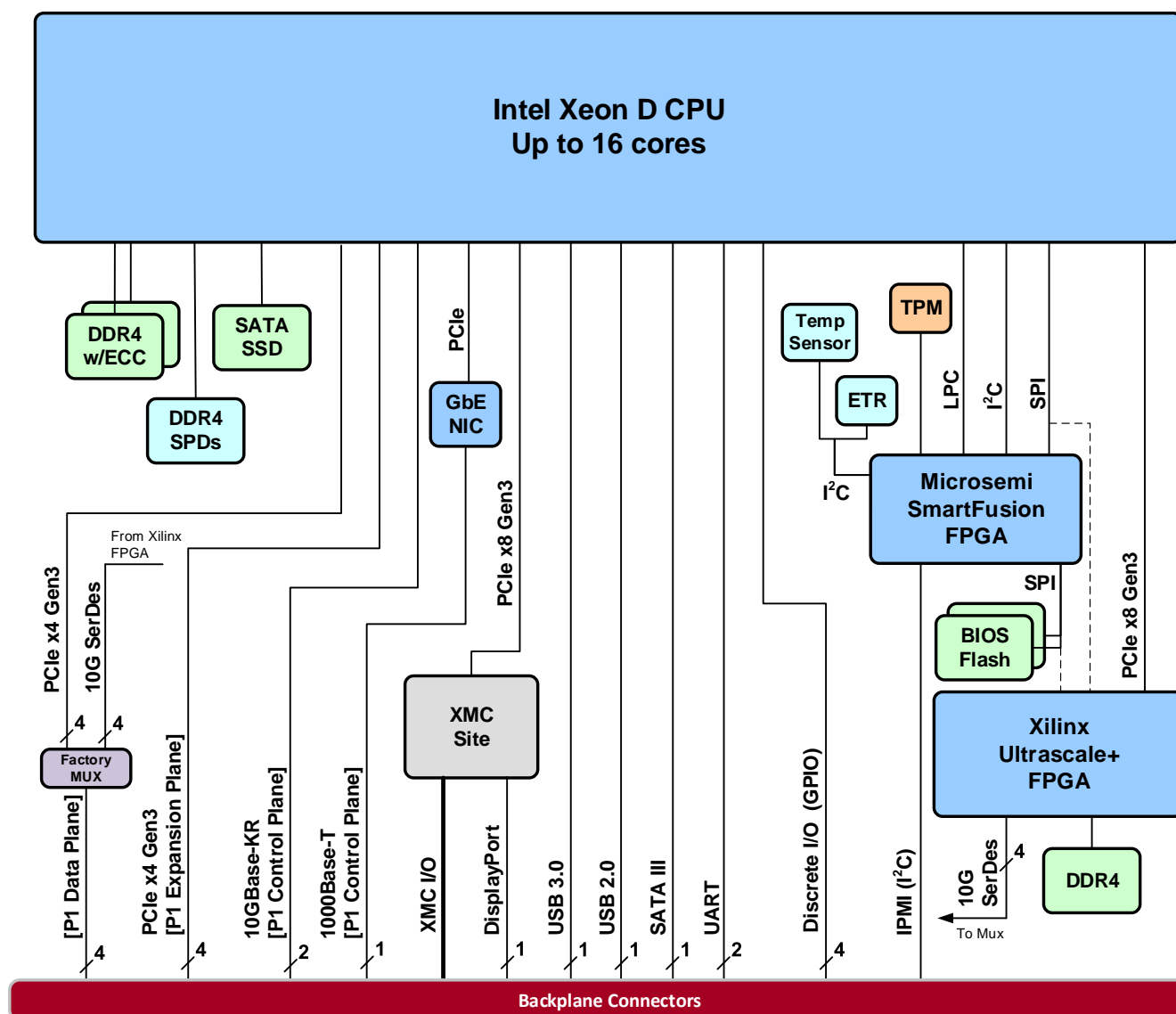


The U-C8770 is designed for High Performance Embedded Computing (HPEC) applications requiring multi-core CPU processing and advanced Cyber Security. It is based on our cutting-edge, award-winning C877 SBC, retaining all of its features and major benefits, while adding full alignment to the Open Group SOSA™ technical standard for easier interoperability, faster time to market and lower integration costs. The U-C8770 also adds optional support for 40GE data plane connectivity to support high-bandwidth video and sensor data transport over Ethernet backplane.

The SBC packs high-end multi-core Intel Xeon-D embedded processor into the rugged and compact 3U VPX form factor. Supporting up to 16 cores/32 threads, it is perfect for applications that require a high level of processing parallelism. Several Intel Xeon D and Pentium CPU options are available, to provide the balance of CPU throughput and board power consumption that are best for your system.

Designed from the ground up with security in mind, the U-C8770 provides a comprehensive custom Cyber Security framework – AiSecure™, which includes both standard and advanced security features, such as Intel CPU security (TXT, AES-NI, etc.), on-board TPM 2.0 (Trusted Platform Module), SSD security with AES 256 Encryption, Quick Erase, and Secure Erase features, Security Manager, tamper detection, and a variety of FPGA protection tools. AiSecure™ also includes security features and encryption that can be implemented using the optional Xilinx UltraScale+ FPGA.

The U-C8770 is designed with a high-bandwidth bus architecture and a versatile assortment of on-board I/O interfaces, with an XMC site for additional I/O options. The board is available in both air-cooled and conduction-cooled versions.



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Board Architecture

Processor	<p>The U-C8770 is available with the following Intel CPU options:</p> <ul style="list-style-type: none">• Xeon D-1577 – 16 cores/32 threads (Hyper-Threading) @ 1.3 GHz, 2.1 GHz Turbo Boost, 24 MB Cache• Xeon D-1559 – 12 cores/24 threads (Hyper-Threading) @ 1.5 GHz, 2.1 GHz Turbo Boost, 18 MB Cache• Pentium D-1519 – 4 cores/8 threads (Hyper-Threading), @ 1.5 GHz, 2.1 GHz Turbo Boost, 6 MB Cache <p>Note: The Pentium D-1519 CPU option supports only PCIe Gen2 maximum speeds and does not support PCIe Gen3</p> <p>Supports Virtualization Technology (VT-x), AES-NI, Secure Key RNG, Trusted Execution Technology (TXT), AVX2</p>		
Xilinx UltraScale+ FPGA	<p>Optional Xilinx Zynq UltraScale+ FPGA w/ARM CPU, linked to the Xeon CPU via PCIe x8 Gen3, enables implementation of 40GE data plane, I/O customizations and on-board security features. Available options include:</p> <ul style="list-style-type: none">• ZU4CG – 192k System Logic Cells• ZU7CG – 504k System Logic Cells		
Board Resources	<ul style="list-style-type: none">• Watchdog Timers (Windowed & Standard)• Real Time Clock• PCIe Non-Transparent (NT) Port support	<ul style="list-style-type: none">• Temperature Sensors• Elapsed Time Recorder• General Purpose DMA Engine	<ul style="list-style-type: none">• VITA 46.11 Tier 1 & Tier 2 IPMI (Intelligent Platform Management Interface)
SOSA™ Slot Profiles	<p>The following SOSA™ slot profiles are supported by standard configurations of the U-C8770:</p> <ul style="list-style-type: none">• SLT3-PAY-1F1F2U1TU1T1U1T-14.2.16 3U I/O Intensive SBC PICP <p>Supports all 4 associated module profiles (Dash Num options) of MOD3-PAY-1F1F2U1TU1T1U1T-16.2.15-n</p> <ul style="list-style-type: none">• PCIe data/expansion plane supports up to Gen2 or Gen3, depending on the CPU option selected• Ethernet data plane support uses the optional Xilinx UltraScale+ FPGA (it must be installed). An FPGA IP core for 10GE/40GE Ethernet support can be provided upon customer request• Support for DisplayPort interface requires a compatible add-on graphics XMC <p>Note: please contact your Aitech sales representative to get more information on the above capabilities and ordering options</p>		

AiSecure™ Cyber Security

Intel & BIOS Security	<ul style="list-style-type: none">• Secure Boot based on Intel TXT, TPM 2.0• Trusted Platform with SmartFusion™ Security Manager• BIOS Modification Protection• Boot Guard		
FPGA Security	<ul style="list-style-type: none">• Xilinx Device DNA• Bitstream Encryption	<ul style="list-style-type: none">• Bitstream Authentication• Readback/JTAG Disable	<ul style="list-style-type: none">• Readback CRC• Keyclear and IPROG
On-board SSD Security	<ul style="list-style-type: none">• Secure Erase• Quick Erase• Disk Data Encryption utilizing AES 256 keys (Self Encrypted Disk), TCG OPAL 2.0		
Anti-Tamper Protection	<ul style="list-style-type: none">• Battery backed tamper detection signal for system level protection• Tamper resistant FPGAs with bit stream authentication and read back limitation		

Note: For more information on AiSecure™, please see the Aitech Cybersecurity Whitepaper.

Memory Resources

RAM	Up to 32 GB of DDR4 SDRAM in dual channel configuration with ECC, operating at 2133MT/s
SATA SSD	Up to 1 TB on-board SATA SSD, SLC & MLC Flash options with AES 256 Encryption, Quick Erase, and Secure Erase Standard options are listed in <i>Ordering Information</i> below (additional SLC & MLC options may be available per customer request, contact an Aitech representative for more info)
Boot Flash	Dual BIOS Flash devices (Primary device for normal board operation, alternate device for board maintenance)

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I/O	I/O Variant ⁽¹⁾	
	Variant #1	Variant #2
Data Plane	PCIe Gen2/Gen3 ⁽²⁾⁽³⁾ x4	10GBase-KX4 / 40GBase-KR4 ⁽⁴⁾
Expansion Plane	PCIe Gen2/Gen3 ⁽²⁾ x4	
Control Plane	2x 1000Base-KX / 10GBase-KR ⁽²⁾	
	1x 1000BaseT	
USB	1x USB 3.0/2.0 ⁽²⁾	
	1x USB 2.0	
SATA	1x SATA Gen2/Gen3 ⁽²⁾	
Serial Ports	2x RS-232/422/485 ⁽²⁾	
Discrete I/O (GPIO)	4x Single-Ended	
XMC I/O Pattern (per VITA 46.9)	P1w9-X12d+X16s+X8d	

Notes: (1) The I/O Variants and port modes offer compatibility to different "Dash Num" options within the SOSA™ and VITA 65.0/65.1 MOD3-PAY-1F1F2U1T1U1T-16.2.15-n module profiles; additional I/O options may be available per customer request, contact an Aitech representative for more information

(2) Port mode is software configurable

(3) PCIe Gen3 requires Xeon-D 1559 or 1577 CPU options (Pentium-D 1519 is limited to PCIe Gen2 only)

(4) Ethernet data plane connectivity requires the optional Xilinx UltraScale+ FPGA to be installed (see Ordering information below). An appropriate FPGA IP core is available per customer request. Please contact an Aitech representative for more information.

XMC Slot

PCIe x8 Gen3 port supporting x8/x4/x2/x1 port widths and Gen3/Gen2/Gen1 speeds (depending on XMC capabilities), 12V VPWR

Software

Operating Systems	Windows®, VxWorks®, and Linux® are supported RedHawk™ Real-Time Linux support can be provided upon customer request
Drivers	Operating system specific device drivers for board resources are available
BIT	Built-In Tests are available

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Mechanical

	Form Factor & Dimensions	Pitch	Weight
Air-Cooled	3U VPX REDI per ANSI/VITA 48.1	1"	< 800 g [1.77 lbs.]
Conduction-Cooled 2LM	3U VPX REDI 2LM (Two Level Maintenance) per ANSI/VITA 48.2	1"	< 1130 g [2.49 lbs.]

Power

CPU Option	+12 V	+3.3 V_AUX	Total ⁽¹⁾ ⁽²⁾	PassMark CPUMark
Pentium D-1519 (4 core)	2.23 A	0.27 A	27.65 W	3230
Xeon D-1559 (12 core)	4.53 A	0.27 A	55.25 W	10116
Xeon D-1577 (16 core)	4.63 A	0.27 A	56.45 W	10335

- Notes:
- (1) Power measured during PassMark CPUMark test at room temperature
Tested with 16GB RAM, with on-board SATA SSD, without the optional Xilinx UltraScale+ FPGA, no XMC installed
 - (2) Power consumption is determined by CPU option, RAM capacity option, Xilinx UltraScale+ FPGA option, etc. and varies according to CPU load and utilization of CPU cores. Contact an Aitech representative for configuration specific power consumption.

Environmental

Specs per VITA 47	Air-Cooled			Conduction-Cooled	
	Commercial	Rugged	Military	Rugged	Military
Operating Temp.	AC1 (0 to +55°C) ⁽²⁾	AC3 (-40 to +70°C) ⁽²⁾	AC4 (-40 to +85°C) ^(1,2)	CC3 (-40 to +70°C) ⁽³⁾	CC4 (-40 to +85°C) ^(1,3)
Non-Operating Temp.	C1 (-40 to +85°C)	C3 (-50 to +100°C)	C4 (-55 to +125°C)	C3 (-50 to +100°C)	C4 (-55 to +125°C)
Vibration	V1	V2	V2	V3	V3
Operating Shock	OS1	OS1	OS1	OS2	OS2
Altitude	15,000 ft.	35,000 ft.	70,000 ft.	35,000 ft.	70,000 ft.
Relative Humidity ⁽⁴⁾	0 - 90%	0 - 95% with Acrylic (Standard), 0 - 100% with Urethane (Optional)			
Conformal Coating	N/A				

- Notes:
- (1) -55°C available, contact an Aitech representative for more information
 - (2) Operating ambient air temperature (with sufficient airflow)
 - (3) Operating card edge temperature
 - (4) Non-condensing

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Ordering Information

Processor
H = Xeon D-1577 (16 core @ 1.3 GHz)
S = Xeon D-1559 (12 core @ 1.5 GHz)
U = Pentium D-1519 (4 core @ 1.5 GHz)

I/O Variant
See I/O section above
1 = PCIe data plane
2 = Ethernet data plane
9 = Custom

Xilinx UltraScale+ FPGA
0 = None
Cannot be 0 if I/O Variant is 2
1 = ZU4CG
3 = ZU7CG

Reserved

U-C8770

Ruggedization
1 = Commercial
2 = Rugged
4 = Military

Cooling + Pitch
Air-Cooled
A3 = 1"
Conduction-Cooled
R4 = 1" 2LM

SDRAM
C = 16 GB
D = 32 GB

SATA SSD
0 = None
D = 128 GB MLC, QE, SE
5 = 256 GB SLC, QE, SE
G = 1 TB MLC, QE, SE
SLC: Single Level Cell; MLC: Multi Level Cell; QE: Quick Erase; SE: Secure Erase

Configuration Number
To be assigned by Aitech

Example: 4U-C8770S-R40D1530-00

Optional Accessories

U-TM8770 Rear Transition Module (RTM) providing convenient access to U-C8770 I/O interfaces via standard connectors and to all XMC I/O via headers. Supports both air and conduction-cooled U-C8770 when installed in a compatible system.

See the U-TM8770 datasheet for more information.

Contact Aitech

Contact your Aitech sales representative for additional product information, and for inquiries regarding customized configurations of the U-C8770, as well as additional software support.

PRELIMINARY

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